ST2082 Interface

Overview

SMPTE ST 2082-1 defines a bit-serial data interface for the transport of 12 Gb/s component digital signals or packetized data. The mapping of various source image formats to the bit-serial data structure defined here in this document is described in additional SMPTE ST 2082 document. The SMPTE ST 2082 suite of documents defines the mapping of various source image formats onto a singlelink, dual-link and quad-link serial digital interface operating at a nominal rate of 12 Gb/s. This informative "overview" describes the documents in the SMPTE ST 2082 suite. The indivi tionally accompany the R′G′B′, R′FSG′FSB′FS, Y′C′BC′R or ICTCP video signal. Interfaces con aining the auxiliary. Could you let me know if IN1 (Diff 100 ohm) port can support SMPTE ST-2082-1 (12G) or not?

Regards, Nicky Yes LMH1219 IN1 100-ohm differential input does support SMPTE.

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Article Content

Aug 31, 2025

PDF Download SMPTE ST 2082-10:2018: SMPTE Standard

Abtract: This standard defines the mapping of: • Mode 1: 2160-line Standard Dynamic Range (SDR) and High Dynamic Range (HDR) Source image formats and ancillary data into a 12G

May 08, 2026

SMPTE ST 2082-1 PDF

SMPTE ST 2082-1 - 12 Gb/s Signal/Data Serial Interface - Electrical This standard describes the electrical and physical characteristics of a 12G-SDI coaxial cable interface suitable for

Jul 07, 2025

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SMPTE ST 2082-1 - 12 Gb/s Signal/Data Serial Interface - Electrical This standard describes the electrical and physical characteristics of a 12G-SDI coaxial cable interface suitable for

Nov 18, 2025

Download SMPTE ST 2082-10 In PDF

SMPTE ST 2082-10 - 2160-Line Source Image and Ancillary Data Mapping for 12G-SDI This Standard defines the mapping of:Mode 1: 2160-line Source image formats and ancillary data

May 15, 2026

SMPTE ST 2082-1 : 2015 AMD 1 2016

JITTER MEASUREMENT PROCEDURES IN BIT-SERIAL DIGITAL INTERFACES SMPTE ST 297-2 : 2017 MULTI-LINK AND MULTI-CHANNEL 1.5G, 3G, 6G AND 12G-SDI USING

Apr 04, 2026

SMPTE ST 2082-11 PDF

This Standard also defines the carriage of the SMPTE ST 352 payload ID''s for the Dual-link 12Gb/s SDI interface is not necessary for implementations to include support for all formats that

Feb 16, 2026

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This standard describes the electrical and physicalcharacteristics of a 12G-SDI coaxial cable interface suitable forapplications where the signal loss does not exceed an amountspecified by

Mar 06, 2026

SMPTE STANDARD

Four parallel 20-bit interfaces of the same line and frame structure, having frame, line and word synchronization and each constructed of two 10-bit data streams, data stream one and data stream

Apr 04, 2026

LMH1219: Can LMH1219 IN1 support SMPTE ST-2082-1 (12G)?

Part Number: LMH1219 Other Parts Discussed in Thread: LMH1218 Hello, Could you let me know if IN1 (Diff 100 ohm) port can support SMPTE ST-2082-1 (12G) or not

Jun 28, 2025

12G-SDI Bit-Serial Interfaces

The SMPTE ST 2082 suite of documents defines the mapping of various source image formats onto a single-link, dual-link and quad-link serial digital interface operating at a nominal rate of 12 Gb/s. This

Sep 22, 2025

ST 2082-10:2015

Abstract: This Standard defines the mapping of: • Mode 1: 2160-line Source image formats and ancillary data into a 12 Gb/s SDI bit-serial interface — This Standard also

Aug 11, 2025

12 Gb/s Signal/Data Serial Interface

1 Scope The purpose of this amendment is to change the Timing Jitter specification for the interface to correctly reflect the requirements and capabilities of 12G-SDI equipment. It also corrects arithmetic

Apr 29, 2026

LMH1219: Can LMH1219 IN1 support SMPTE ST-2082-1 (12G)?

Yes LMH1219 IN1 100-ohm differential input does support SMPTE. Please note we need to use either SMBus or SPI interface to setup the registers as noted below:

May 14, 2026

12 Gb/s Signal/Data Serial Interface — Electrical

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For this interface, the source data shall be the 12G-SDI 10-bit Multiplex as defined for example in SMPTE ST 2082-10, of the form illustrated in the diagram of Figure 1 and Figure 2,

Apr 04, 2026

SMPTE ST 2082-10 PDF

SMPTE ST 2082-10 - 2160-Line Source Image and Ancillary Data Mapping for 12G-SDI This Standard defines the mapping of:Mode 1: 2160-line Source image formats and ancillary data into a 12 Gb/s

Dec 25, 2025

SMPTE 2082-0

The SMPTE ST 2082 series specify a common virtual interface that is carried on both electrical and optical physical interfaces which are also defined in the document suite. The diagram of Figure 1

Dec 16, 2025

SMPTE ST 2082-1 PDF

This standard describes the electrical and physical characteristics of a 12G-SDI coaxial cable interface suitable for applications where the signal loss does not exceed an amount specified

May 07, 2026

SMPTE 2082-0

The SMPTE ST 2082 suite of documents defines the mapping of various source image formats onto a singlelink, dual-link and quad-link serial digital interface operating at a nominal rate of 12 Gb/s.

Nov 06, 2025

SMPTE OVERVIEW 12G-SDI Bit-Serial Interfaces — Overview for the

The SMPTE ST 2082 series specify a common virtual interface that is carried on electrical physical interfaces which are also defined in the document suite and on optical physical interfaces.

Jan 30, 2026

12 GB/S SIGNAL/DATA SERIAL INTERFACE

Document History SMPTE ST 2082-1 12 GB/S SIGNAL/DATA SERIAL INTERFACE - ELECTRICAL

Apr 24, 2026

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Revision of ST 2082-10:2015 SMPTE STANDARD 2160-line and 1080

The 80-bit virtual interface is multiplexed onto a 12G-SDI 10-bit interface. The data streams are multiplexed in the order data stream eight, data stream four, data stream six, data stream two, data

Nov 10, 2025

2160-line Source Image and Ancillary Data Mapping for 12G-SDI

The 80-bit virtual interface shall include sync and timing (TRS) words, Cyclic redundancy code (CRC) words, line numbers, HANC and VANC data and time code – except audio – according

Jun 26, 2025

12G-SDI Bit-Serial Interfaces Overview for the SMPTE ST 2082

The SMPTE ST 2082 series specify a common virtual interface that is carried on both electrical and optical physical interfaces which are also defined in the document suite.

Dec 03, 2025

SMPTE ST 2082-1:2023 Revision of SMPTE ST 2082-1:2015 12 Gb/s

SMPTE ST 2082-1 defines a bit-serial data interface for the transport of 12 Gb/s component digital signals or packetized data. The mapping of various source image formats to the bit-serial data

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